



**Substitute for Form 1449 A & B/PTO**

## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

*(use as many sheets as necessary)*

Sheet

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of

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*Complete if Known*

Application Number	09/750,688
Confirmation Number	8185
Filing Date	January 02, 2001
First Named Inventor	Masashi HACHINOTA
Art Unit	2661
Examiner Name	Unknown

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## FOREIGN PATENT DOCUMENTS

#### **OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation <sup>6</sup>
TB		4. H. KIM et al., "A High-Speed ATM Switch Architecture Using Random Access Input Buffers and Multi-Cell-Time Arbitration", <i>IEEE</i> , Vol. 1, November 3, 1997, pp. 536-540	

**Examiner Signature**

the End

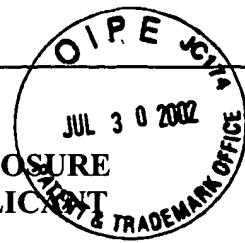
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Attorney Docket Number	O62534

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